

ABSTRACT:

In a transponder (1) and an integrated circuit (5), the integrated circuit (6) has a monitoring circuit (23) to which a d.c. supply voltage (VS) can be fed and by which a signalizing signal (POK) whose waveform is dependent on the relationship between the d.c. supply voltage (VS) and a voltage threshold value (VTHR1, VTHR2) can be generated, wherein the monitoring circuit (23) is arranged to be controllable in respect of the generation of the signalizing signal (POK), and wherein a control circuit (28) is provided for controlling the monitoring circuit (23) by means of at least one control signal (CS1, CS2).

Fig. 1.

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
12 February 2004 (12.02.2004)

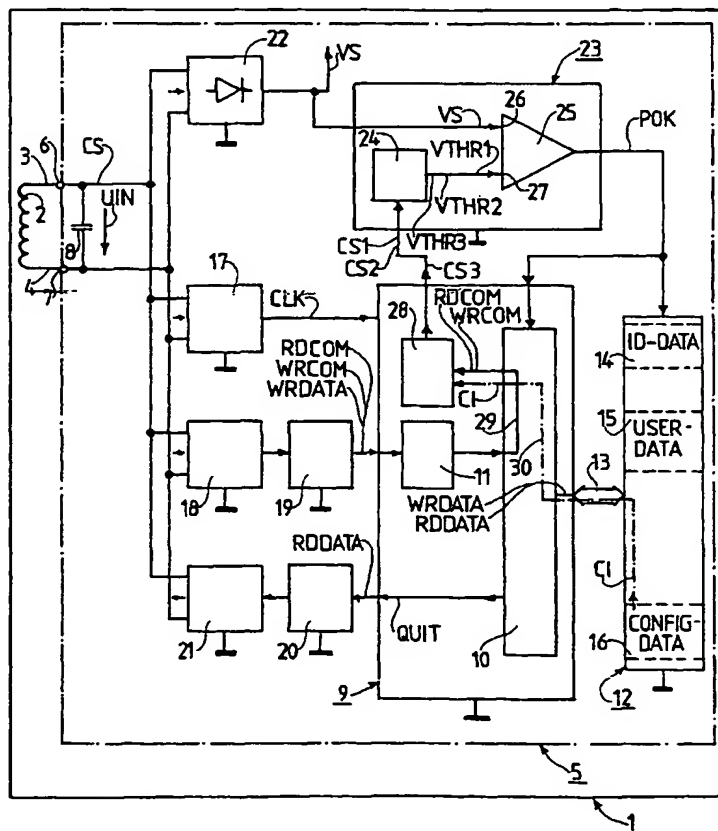
PCT

(10) International Publication Number
WO 2004/013806 A1

- (51) International Patent Classification⁷: **G06K 19/07** (74) Agent: WEBER, Helmut; Philips Intellectual Property & Standards, Triester Strasse 64, A-1101 Vienna (AT).
- (21) International Application Number: PCT/IB2003/003273 (81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (22) International Filing Date: 18 July 2003 (18.07.2003) (84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data: 02102076.3 30 July 2002 (30.07.2002) EP
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(54) Title: TRANSPONDER WITH A CONTROLLABLE POWER-ON-RESET CIRCUIT



(57) Abstract: In a transponder (1) and an integrated circuit (5), the integrated circuit (6) has a monitoring circuit (23) to which a d.c. supply voltage (VS) can be fed and by which a signaling signal (POK) whose waveform is dependent on the relationship between the d.c. supply voltage (VS) and a voltage threshold value (VTHR1, VTHR2) can be generated, wherein the monitoring circuit (23) is arranged to be controllable in respect of the generation of the signaling signal (POK), and wherein a control circuit (28) is provided for controlling the monitoring circuit (23) by means of at least one control signal (CS1, CS2).